Join Intel for a Technical Talk
Logic Technology Development is hiring
Presented by Michael Painter and Harry Gomez

Date: Friday, October 23rd
Time: 3:30 pm – 5:30 pm
Location: AVC 147 inside the CMS annex

We hire PhD in Chem E, Mech E, Mat Sci, EE, CS, CE, Chemistry, most all Engineering and Science related disciplines.

Michael is an Engineering TD Manger in the D1D ramp organization under the Logic Technology Division. He joined Intel in 1996, working as a lithography process engineer in the early transfer of P854. He has held various technical and management positions in the lithography group. Michael has been involved in the ramp and transfer of 11 separate logic technologies. He received both his undergraduate and graduate degrees in chemical engineering from Brigham Young University and has received an Intel Achievement Award.

Harry was born in San Juan Puerto Rico. Joined Intel in 2002, working as a process engineer in the Portland Technology Development group. Received a bachelor's degree in Chemistry from University of Puerto Rico in 1997. Received a Ph.D. in Physical Chemistry from University of California at Berkeley in 2002. He has been awarded three patents and two Intel Achievement Awards.