

UNIVERSITY OF SOUTH FLORIDA

Defense of a Master's Thesis

Methodologies to Exploit ATPG Tools for De-camouflaging

by

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For the MSCP degree in Computer Science & Engineering

Semiconductor supply chain is increasingly getting exposed to Reverse Engineering (RE) of Intellectual Property (IP). Camouflaging of gates are typically employed to hide the gate functionality to prevent RE. Adversaries perform RE by developing custom software to determine test patterns and analyze the outputs. We propose a methodology to RE the camouflaged design can be performed by exploiting the features of commercial/publicly available Automatic Test Pattern Generation (ATPG) tools. The proposed methodology can also assess the strength of camouflaging techniques.

07/27/2016

2:00pm-3:00pm

ENB 313

THE PUBLIC IS INVITED

Examining Committee

Dr. Srinivas Katkoori, Ph.D., Major Professor
Dr. Swaroop Ghosh, Ph.D., Co-Major Professor
Dr. Sriram Chellappan, Ph.D.

*Robert Bishop, Ph.D.
Dean, College of Engineering*

*Dwayne Smith, Ph.D.
Dean, Office of Graduate Studies*

Disability Accommodations:

If you require a reasonable accommodation to participate, please contact the